

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): John S. Yates, Jr., et al.

Serial No.: 09/239,194

Filed: January 29, 1999

Title: COMPUTER WITH A NON-NATIVE OPERATING SYSTEM

Art Unit: 2127

Examiner: Kenneth Tang

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Technology Center 2100

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant wishes to make of record the items listed on the accompanying Form PTO-1449. Applicant respectfully requests the Examiner to fully consider the items and independently ascertain their teaching before issuance of the next action, and to make them of record in the file. The Examiner is also requested to initial and return a copy of the enclosed Form PTO-1449 to evidence such consideration.

This IDS supplements the IDS filed on November 25 and October 17, 2003.

The references listed on the enclosed Form 1449 are references that have come to light in applications listed in the Information Disclosure Statement of November-December 2000. Applicant has not reviewed all of them in detail. Of those that have been reviewed, none of the references are believed to be any more pertinent than the references provided in earlier IDS' and Forms 1449. However, in an abundance of caution, Applicant requests that they be considered.

No fee is due for this Information Disclosure Statement because the fee was paid with an Information Disclosure Statement filed October 17, 2003.

The Commissioner is hereby authorized to charge any additional fees that may be required for this Information Disclosure Statement, or credit any overpayment, to Deposit Account 23-2405, Order No. 114596-05-4013.

Respectfully submitted,

WILLKIE FARR & GALLAGHER, LLP

Dated: June 15, 2004

By: 

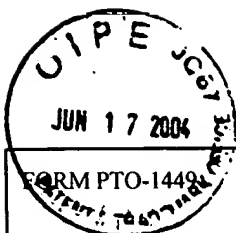
David E. Boundy

Registration No. 36,461

I certify that this correspondence, along with any documents referred to therein, is being deposited with the United States Postal Service on June 15, 2004 as First Class Mail in an envelope with sufficient postage addressed to The Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



WILLKIE FARR & GALLAGHER, LLP
787 Seventh Ave.
New York, New York 10019
(212) 728-8757
(212) 728-9757 Facsimile



FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)	ATTY. DOCKET NO. 114596-05-4013	SERIAL NO. 09/239,194
	APPLICANT John S. Yates, Jr., et al.	
	FILING DATE January 29, 1999	GROUP ART UNIT 2127

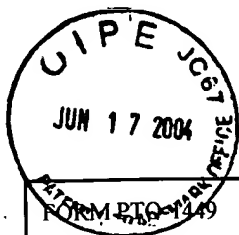
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U.S. PATENT DOCUMENTS

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,307,504	04/26/1994	Robinson	395	800	Mar. 7, 1991
	5,339,422	8/16/1994	Brender	714	4	Mar. 7, 1991
	5,355,487		Keller			
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EXAMINER			DATE CONSIDERED			



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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)			
		✓	Ammons, Ball and Larus: Exploiting Hardware Performance Counters with Flow and Context Sensitive Profiling, Proceedings of the ACM SIGPLAN 1997 Conference on Programming Language Design and Implementation, pp. 85-96 (1997)
			Compaq Computer Corp., Compiler Writer's Guide for the Alpha 21264 (1999) www.alphalinux.org/archives/axp-list/June1999/0475.html
		✓	Reiner W. Hartenstein, Jürgen Becker: Performance Analysis in CoDe-X Partitioning for Structural Programmable Accelerators; Proc. of 5th Int'l Workshop on Hardware/Software Co-Design CODES/CASHE '97, Braunschweig, Germany, p. 141 (March 1997)
		✓	Hollingsworth, Critical Path Profiling of Message Passing and Shared-Memory Programs, IEEE Transactions on Parallel and Distributed Systems vol. 9, no. 10, pp. 1029-1040 (October 1998)
		✓	Intel Corporation, Intel Processor Family Developer's Manual (1997), pages 1-1 to 1-6, 2-1 to 2-20
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		✓	Jones, Puzzling with Microcode, ACM SIGARCH Computer Architecture News, vol. 11, no. 5, pp. 8-12 (1983)
		✓	Magnusson and Werner, Efficient Memory Simulation in SimICS, Proceedings of the 28th Annual Simulation Symposium, IEEE, pp. 62-73 (1995).
		✓	Mueller, Rustagi and Baker: MiThOS - A Real-Time Micro-Kernel Threads Operating System, IEEE Real-Time Systems Symposium 1995: pp. 49-55 (1995)
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EXAMINER		DATE CONSIDERED	